



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Norio Okada, et al.

Serial No.: 10/649,771

Group Art Unit: 2812

Filed: August 28, 2003

Examiner: Tsai, H. Jey

For: SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME

Honorable Commissioner of Patents
Alexandria, VA 22313-1450

REPLACEMENT DRAWING SHEETS INCLUDING DRAWING CORRECTIONS

Sir:

Submitted herewith are corrected sheets with Figures 17, 18, 19, and 20.

Figure 17 is amended by changing the reference designation "71; seal ring" to "70" to agree with the specification at page 41, line 11.

Figure 18 is amended by redirecting the lead line from reference numeral 70A to point to the exposed face of assembly pad 70, to agree with the specification at page 45, line 13.

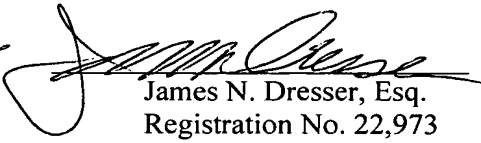
Figure 19 is amended by changing the reference designation "79; bottomed portion of conductive layer" to "79; bottom portion of conductive layer" to agree with the specification at, *inter alia*, page 46, lines 16-17.

Figure 20 is amended by redirecting the lead line from reference numeral 70A to point to the exposed face of assembly pad 70, to agree with the specification at page 48, lines 22-23 and page 43, line 3.

Serial No. 10/649,771
Docket No. NO3409US

Copies of these figures having the corrections marked in red are also attached.

Respectfully Submitted,

Date: October 18, 2004 
James N. Dresser, Esq.
Registration No. 22,973

McGinn & Gibb, PLLC
8321 Old Courthouse Road, Suite 200
Vienna, VA 22182-3817
(703) 761-4100
Customer No. 21254